Microprocessor 8086: Architecture, Programming and Interfacing

Sunil Mathur

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Primarily intended for the undergraduate students of electronics and communication engineering, computer science and engineering, and information technology, this book skilfully integrates both the hardware and software aspects of the 8086 microprocessor. It offers the students an up-to-date account of the state-of-the-art microprocessors and therefore can be regarded as an incomparable source of information on recently developed microprocessor chips. The book covers the advanced microprocessor architecture of the Intel microprocessor family, from 8086 to Pentium 4.

The text is organized in four parts. Part I (Chapters 1–7) includes a detailed description of the architecture organization, instruction set, and assembler directives of microprocessor 8086. Part II (Chapters 8–11) discusses the math coprocessor, multiprocessing and multiprogramming, the different types of data transfer schemes, and memory concepts. Part III (Chapters 12–15) covers programmable interfacing chips with the help of extensive interfacing examples. Part IV (Chapters 16–18) deals with advanced processors—from 80186 to Pentium 4.

This well-organized and student-friendly text should prone to be an invaluable asset to the students as well as the practising engineers.

KEY FEATURES
• Gives elaborate programming examples to develop the analytical ability of students.
• Provides solved examples covering different types of typical interfacing problems to develop the practical skills of students.
• Furnishes chapter-end exercises to reinforce the understanding of the subject.

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